

ABSTRACT OF THE DISCLOSURE

A driving apparatus for a display panel capable of reducing a circuit scale while suppressing the drop of a contrast includes a scan driver having a first power source for generating a first voltage, generating a scan pulse for bringing the capacitive light emission device to either one of an ON state and an OFF state based on the first voltage, and applying the scan pulse to the row electrode, a sustain driver having a second power source for generating a second voltage, generating a sustain pulse for allowing the capacitive light emission device set to the ON state to emit light based on the second voltage, and applying the scan pulse to the row electrode, and a reset driver generating a reset pulse for initializing the state of the capacitive light emission device based on the sum of the first voltage generated by the first power source and the second voltage generated by the second power source, and applying the reset pulse to the row electrode. This circuit construction can eliminate the necessity of a dedicated power source for generating the reset pulse. In another aspect of the invention, a reset pulse having a waveform having a sharp level shift at a front edge thereof and a gentle level shift at a portion succeeding the front edge is generated based on a voltage generated by connecting in series a power source for generating a sustain discharge pulse and a power source for generating a scan pulse. This circuit construction can eliminate the necessity for a

dedicated power source for generating the reset pulse and can lower light emission brightness resulting from reset discharge induced in accordance with the reset pulse.